



single chip for 2 amplifiers

$U1, U2 \rightarrow$  TLV2462IP

$D1, D2, D3 \rightarrow$  BPV22NF

photo diode array

$C1 \rightarrow 5pf$

$C2 \rightarrow 10uf$

$R1 \rightarrow 100k$

$R2, R4 \rightarrow 47k$

$R3 \rightarrow 3k$

- row 1L is all ground

- left 9 and right 10 are combined photo diode output