

# MODBUS Protocol for Energy Storage Inverter

## Revision Record

S/N	Revision Content	Revised by	Revised on	Ver. No.
1	1. Two registers (with inverter fault state, charging state, and unique ID) defined by RGSC are increased. 2. Units of minimum, maximum, and default values are removed (for protocol conversion code). 3. The BMS enable register and BMS protocol register are increased. 4. The charging time and discharging time registers are increased (to achieve timed charging and discharging). 5. The state register is removed (not available and memory occupied). 6. The protocol structure is modified (refer to the controller protocol).	zhengkk	July 14, 2021	V1.4
2	1. The definition of the current state value (8: battery activation, 9: manual shutdown, 10: fault) of the machine is modified. 2. The default values of some loop parameters are set to 4096. When used in the program, 4096 is used as the default value. 3. The battery type is GEL (3) by default. If there is a difference in the program, it may be customized according to the customer ID. 4. The original Baud rate is changed to Parallel Mode. 5. The output priority is 2 (SBU) by default. If there is a difference in the program, it may be customized according to the customer ID.	zhengkk	September 16, 2021	V1.5
3	The Modbus protocol format specification and the register address table are merged into a single file. Note: 1. If the version No. on the page is incorrect, you only need to modify the table name. The title and version No. at the header are automatically updated without manual modification. 2. When releasing the version with neutral packing, you need to replace the company name at the page of the two files with "protocol", and do not delete the original characters; otherwise, the format will change when the company name is added next time.	zhengkk	September 24, 2021	V1.5
4	1. The protocol is revised, and the register is increased to support single split-phase machine, two-way PV input and three-way AC power input, and three-way inverter output data transmission. 2. E218 register address is added to set the derated power of the machine.	wangqt	June 14, 2022	V1.6
5	1. The time of segmental charging and discharging and their enable settings are increased. 2. The settings of grid-connected generation and leakage detection are increased.	wangzw	June 1, 2022	V1.7
6	1. The single split-phase machine borrows the adjustment parameter addresses of the PLL, DF43 and DF44, to adjust the iteration control parameters; and the data type is changed to the signed number, and the default value is changed. 2. The maximum value of boost charge time E102 is changed to 900, consistent with the range set on the display. 3. The E21F address is added to set the grid-connected PF value. 4. The data annotation error in the E004 battery type and address (12-L13 and 13-L14) is fixed. 5. The error cumulative charging unit and mismatch of proportion and actual quantity of AC power are fixed, and the cumulative charging unit is changed to the same as the charging unit on the day, which is AH. 6. The 0x214 address is changed back to the AC power phase-A current (generation-3 parallel machine also uses this address as the parallel current), and 0x229-0x220 are increased as the power phase B and phase C.	wangqt	July 28, 2022	V1.7
7	1. E00F is used for discharge cutoff SOC setting and is valid in BMS communication. 2. E01C is used to set the current for the lithium battery to stop charging. 3. E01D is used to set the SOC for the lithium battery to stop charging. 4. E01E is used to set the low SOC capacity alarm and is valid for BMS communication. 5. E01F is used to change the SOC capacity setting of the AC power in SBU mode and is valid for BMS communication. 6. E020 is used to change the SOC capacity setting of the inverter in SBU mode and is valid for BMS communication.	zhengkk	August 2, 2022	V1.7
8	1. E207 is changed to enable the N wire grounding, which is available only for some models. 2. The number of historical fault records is increased to 32.	zhengkk	November 11, 2022	V1.80
9	1. The register for grid-connected voltage protection is increased. 2. Grid-connected active, reactive, and PF registers are increased. 3. Grid-connected power register is increased. 4. The insulation impedance detection enable and threshold setting registers are increased. 5. The grid-connected current F02C on the day is increased.	zhengkk	February 13, 2023	V1.90
10	1. The PV output priority is increased. 2. Grid-connected parameters are independently placed in group 08.	zhengkk	March 7, 2023	V1.91

11	1. The DC load switch is increased.	zhengkk	March 8, 2023	V1.92
12	1. Diesel engine operating mode and diesel engine charging current setting parameters are increased. 2. The function settings of battery participating in grid connection are increased. 3. The grid-connected active power is changed to the actual power. 4. Diesel engine voltage calibration coefficient is increased.	zhengkk	August 4, 2023	V1.93
13	1. The battery temperature register 0×0103 is increased. 2. 0×E037 register is changed to an operating mode register. 3. 0×E03A is modified to enable battery temperature compensation. 4. The SOC value corresponding to the charge and discharge period (0×E03B–0×E040) is added. 5. 0×E204 is changed to bms communication fault stop register. 6. Diesel engine rated power setting 0×E221 is increased. 7. The CT ratio register 0×E42B is increased. 8. Anti-reverse and anti-error power setting register 0×E42C is increased.	zhengkk	October 8, 2023	V1.94
14	1. A/B/C phase home load register is increased. 2. The battery voltage determination register for the timed charging and discharging period is increased. 3. The maximum power register for timed discharging is increased. 4. The normal network latency register is increased. 5. The register for normal/reconnected power rise rate is increased. 6. The register for network voltage frequency range is increased.	zhengkk	January 4, 2024	V1.95
15	1. The maximum power register for timed charging is increased. 2. The register for timed charging source selection is increased.	zhengkk	January 11, 2024	V1.96

# Format Specification of the MODBUS Protocol for Energy Storage Inverter

## 1. Document Description

This document defines the content of RS485 communication protocol for the Company's energy storage inverters, including RS485 communication frame format, Modbus register address definition, quantity calibration, etc. The protocol follows the Modbus-RTU protocol and supports 03, 06, and 10 function codes. The maximum number of read-write registers at a time is 32.

## 2. Serial Communication Parameters

"9,600, n, 8, 1" indicates a baud rate of 9,600, with 8 data bits, and no parity check.

There are one host and multiple slaves in RS485 connection mode. The default address of the inverter is 1, which can be set. It supports 255 universal address. When a host and an inverter are connected one to one, 255 can be used to communicate with the inverter. The address that the inverter responds to is the actual address.

## 3. Data Format

Slave IP Address	Function Code		Data Length or Content	CRC Check
1 byte	1 byte		N bytes	2 bytes
Slave IP address range: 01H to FEH Host IP broadcast address: 0 Universal address: FFH	03H	Reading multiple registers	Command related	Check range: all data from the slave IP address to the CRC check; Transmission order: The CRC calculates the result as 16-bit data. In actual transmission, the low byte is passed first, and the high byte is passed later.
	06H	Writing a single register		
	10H	Writing multiple registers		
	Miscellaneous	Invalid		

### 3.1 Reading the data frame format

Frame format sent by the host:

Slave IP Address	Function Code	Data Field					CRC Check	
1 byte	1 byte	4 bytes					2 bytes	
Actual address	03H	High byte of register address	Low byte of register address	N high bytes of registers, usually 00H	N low bytes of registers (N<=32)		CRC_L	CRC_H
1	3	02H	00H	00H	20H		45H	AAH

Data frame format returned from the slave IP:

Slave IP Address	Function Code	Data Field							CRC Check	
1 byte	1 byte	(2*N+1) bytes							2 bytes	
Actual address	03H	Byte length of the returned data	1 byte		1 byte	1 byte	1 byte	1 byte	...	
			Returned data			Register 1 value			...	
			High	Low	High	Low	...		CRC_L	

Error frame format returned from the slave IP:

Slave IP Address	Function Code	Error Code		CRC Check
1 byte	1 byte	1 byte	1 byte	2 bytes
Actual address	83H	See the error code table.		CRC_L   CRC_H

### 3.2 Writing multiple data frame formats

Frame format sent by the host:

Slave IP Address	Function Code	Data Field						CRC Check	
1 byte	1 byte	5+2*N bytes						2 bytes	
		1 byte	1 byte	1 byte	1 byte	1 byte	2*N bytes		
Actual address	10H	Register address		Register count		Data Length	For the value of N registers, the high byte precedes the low byte.	CRC_L	CRC_H
		High byte	Low byte	High byte	Low byte	2*N			

Response frame format returned from the slave IP:

Slave IP Address	Function Code	Data length				CRC Check	
1 byte	1 byte	1 byte	1 byte	1 byte	1 byte	2 bytes	
Actual address	10H	Register address		Register count			
		High byte	Low byte	High	Low	CRC_L	CRC_H

Error frame format returned from the slave IP:

Slave IP Address	Function Code	Error Code				CRC Check	
1 byte	1 byte	1 byte				2 bytes	
Actual address	90H	See the error code table.				CRC_L	CRC_H

### 3.3 Writing a single data frame format

Frame format sent by the host:

Slave IP Address	Function Code	Data Field				CRC Check	
1 byte	1 byte	1 byte	1 byte	1 byte	1 byte	2 bytes	
Actual address	06H	Register address		Register value		CRC_L	CRC_H
		High byte	Low byte	High	Low		

Response frame format returned from the slave IP:

Slave IP Address	Function Code	Data Field				CRC Check	
1 byte	1 byte	1 byte	1 byte	1 byte	1 byte	2 bytes	
Actual address	06H	Register address		Register value		CRC_L	CRC_H
		High byte	Low byte	High	Low		

Error frame format returned from the slave IP:

Slave IP Address	Function Code	Error Code				CRC Check	
1 byte	1 byte	1 byte				2 bytes	
Actual address	86H	See the error code table.				CRC_L	CRC_H

### 3.4 Error code table

Code	Name	Meaning
01H	Illegal command	The slave may not support this command.
02H	Illegal data address	The register address requested by the host is out of the legal register address range defined by the slave.
03H	Illegal data value	The register value requested by the host is out of the register value range defined by the slave.
04H	Operation failure	The parameter write operation is invalid for the parameter setting, or the slave does not support the
05H	Password error	The password is error for the address validation.
06H	Data frame error	The length of the data frame sent by the host is incorrect, and the CRC check bit in RTU format is different from that calculated by the slave.
07H	Parameter read-only	Parameters changed during the host write operation are read-only.
08H	Parameters cannot be modified during operation	The parameters that are modified during the host write operation are the those that cannot be changed during running.
09H	Password protection	When the host is reading or writing, the system is reported to be locked if the password is set and locked.
0AH	Length error	The number of read/write registers exceeds the upper limit 32.

0BH	Permission denied	There is no permission to perform this operation
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## 4. CRC Check Computation

The CRC domain verifies the content of the entire frame, that is, all data from the slave IP address to the CRC check. The slave retests the CRC check data and compares it with the check value in the received data stream to determine the validity of the received data. The CRC domain consists of two-byte and 16-bit binary value data. In actual transmission, the low byte is passed first, and the high byte is passed later.

There are three methods to calculate the CRC check value. If the results of the three methods are the same, you can choose them freely according to the actual situation.

Method 1: cycle computation by bit

```
unsigned int crc_cal_value(unsigned char*data_value,unsigned char data_length)
{
    int i;
    unsigned int crc_value=0xffff;
    while(data_length--)
    {
        crc_value^=*data_value++;
        for(i=0;i<8;i++)
        {
            if(crc_value&0x0001)
                crc_value=(crc_value>>1)^0xa001;
            else
                crc_value=crc_value>>1;
        }
    }
    return(crc_value);
}
```

Method 2: byte lookup table

```
/*CRC value of the high byte*/
static unsigned int auchCRCHi[] =
{
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
```

```

/*CRC value of the low byte*/
static unsigned int auchCRCLo[] =
{
0x00, 0xC0, 0xC1, 0x01, 0xC3, 0x03, 0x02, 0xC2, 0xC6, 0x06, 0x07, 0xC7, 0x05, 0xC5, 0xC4, 0x04,
0xCC, 0x0C, 0x0D, 0xCD, 0x0F, 0xCF, 0xCE, 0x0E, 0x0A, 0xCA, 0xCB, 0x0B, 0xC9, 0x09, 0x08, 0xC8,
0xD8, 0x18, 0x19, 0xD9, 0x1B, 0xDB, 0xDA, 0x1A, 0x1E, 0xDE, 0xDF, 0x1F, 0xDD, 0x1D, 0x1C, 0xDC,
0x14, 0xD4, 0xD5, 0x15, 0xD7, 0x17, 0x16, 0xD6, 0xD2, 0x12, 0x13, 0xD3, 0x11, 0xD1, 0xD0, 0x10,
0xF0, 0x30, 0x31, 0xF1, 0x33, 0xF3, 0xF2, 0x32, 0x36, 0xF6, 0xF7, 0x37, 0xF5, 0x35, 0x34, 0xF4,
0x3C, 0xFC, 0xFD, 0x3D, 0xFF, 0x3F, 0x3E, 0xFE, 0xFA, 0x3A, 0x3B, 0xFB, 0x39, 0xF9, 0xF8, 0x38,
0x28, 0xE8, 0xE9, 0x29, 0xEB, 0x2B, 0x2A, 0xEA, 0xEE, 0x2E, 0x2F, 0xEF, 0x2D, 0xED, 0xEC, 0x2C,
0xE4, 0x24, 0x25, 0xE5, 0x27, 0xE7, 0xE6, 0x26, 0x22, 0xE3, 0x23, 0xE1, 0x21, 0x20, 0xE0,
0xA0, 0x60, 0x61, 0xA1, 0x63, 0xA3, 0xA2, 0x62, 0x66, 0xA6, 0xA7, 0x67, 0xA5, 0x65, 0x64, 0xA4,
0x6C, 0xAC, 0xAD, 0x6D, 0xAF, 0x6E, 0xAE, 0xAA, 0x6A, 0x6B, 0xAB, 0x69, 0xA9, 0xA8, 0x68,
0x78, 0xB8, 0xB9, 0x79, 0xBB, 0x7B, 0x7A, 0xBA, 0xBE, 0x7E, 0x7F, 0xBF, 0x7D, 0xBD, 0xBC, 0x7C,
0xB4, 0x74, 0x75, 0xB5, 0x77, 0xB7, 0xB6, 0x76, 0x72, 0xB2, 0xB3, 0x73, 0xB1, 0x71, 0x70, 0xB0,
0x50, 0x90, 0x91, 0x51, 0x93, 0x53, 0x52, 0x92, 0x96, 0x56, 0x57, 0x97, 0x55, 0x95, 0x94, 0x54,
0x9C, 0x5C, 0x5D, 0x9D, 0x5F, 0x9F, 0x9E, 0x5E, 0x5A, 0x9A, 0x9B, 0x5B, 0x99, 0x59, 0x58, 0x98,
0x88, 0x48, 0x49, 0x89, 0x4B, 0x8B, 0x8A, 0x4A, 0x4E, 0x8E, 0x8F, 0x4F, 0x8D, 0x4D, 0x4C, 0x8C,
0x44, 0x84, 0x85, 0x45, 0x87, 0x47, 0x46, 0x86, 0x82, 0x42, 0x43, 0x83, 0x41, 0x81, 0x80, 0x40,
};

/*function returns CRC as an unsigned short*/
/*parameter puchMsg: the message used to calculate CRC*/
/*parameter usDataLen: the number of bytes in the message*/
unsigned int CRC16(unsigned int * puchMsg,unsigned int usDataLen)
{
    unsigned int uchCRCHi = 0xFF ; /*high byte initialization of CRC*/
    unsigned int uchCRCLo = 0xFF ; /*low byte initialization of CRC*/
    unsigned int uIndex ; /*CRC lookup table index*/
    while (usDataLen--) /*complete the entire message buffer*/
    {
        uIndex = uchCRCLo ^ *puchMsg++ ; /*CalcCRC*/
        uchCRCLo = uchCRCHi ^ auchCRCHi[uIndex] ;
        uchCRCHi = auchCRCLo[uIndex] ;
    }
    return (uchCRCHi << 8 | uchCRCLo) ;
}

```

## Method 3: word lookup table

```

Static unsigned int tblCRC[] =
{
0x0000,0xC1C0,0x81C1,0x4001,0x01C3,0xC003,0x8002,0x41C2,
0x01C6,0xC006,0x8007,0x41C7,0x0005,0xC1C5,0x81C4,0x4004,

```

```

0x01CC,0xC00C,0x800D,0x41CD,0x000F,0xC1CF,0x81CE,0x400E,
0x000A,0xC1CA,0x81CB,0x400B,0x01C9,0xC009,0x8008,0x41C8,
0x01D8,0xC018,0x8019,0x41D9,0x001B,0xC1DB,0x81DA,0x401A,
0x001E,0xC1DE,0x81DF,0x401F,0x01DD,0xC01D,0x801C,0x41DC,
0x0014,0xC1D4,0x81D5,0x4015,0x01D7,0xC017,0x8016,0x41D6,
0x01D2,0xC012,0x8013,0x41D3,0x0011,0xC1D1,0x81D0,0x4010,
0x01F0,0xC030,0x8031,0x41F1,0x0033,0xC1F3,0x81F2,0x4032,
0x0036,0xC1F6,0x81F7,0x4037,0x01F5,0xC035,0x8034,0x41F4,
0x003C,0xC1FC,0x81FD,0x403D,0x01FF,0xC03F,0x803E,0x41FE,
0x01FA,0xC03A,0x803B,0x41FB,0x0039,0xC1F9,0x81F8,0x4038,
0x0028,0xC1E8,0x81E9,0x4029,0x01EB,0xC02B,0x802A,0x41EA,
0x01EE,0xC02E,0x802F,0x41EF,0x002D,0xC1ED,0x81EC,0x402C,
0x01E4,0xC024,0x8025,0x41E5,0x0027,0xC1E7,0x81E6,0x4026,
0x0022,0xC1E2,0x81E3,0x4023,0x01E1,0xC021,0x8020,0x41E0,
0x01A0,0xC060,0x8061,0x41A1,0x0063,0xC1A3,0x81A2,0x4062,
0x0066,0xC1A6,0x81A7,0x4067,0x01A5,0xC065,0x8064,0x41A4,
0x006C,0xC1AC,0x81AD,0x406D,0x01AF,0xC06F,0x806E,0x41AE,
0x01AA,0xC06A,0x806B,0x41AB,0x0069,0xC1A9,0x81A8,0x4068,
0x0078,0xC1B8,0x81B9,0x4079,0x01BB,0xC07B,0x807A,0x41BA,
0x01BE,0xC07E,0x807F,0x41BF,0x007D,0xC1BD,0x81BC,0x407C,
0x01B4,0xC074,0x8075,0x41B5,0x0077,0xC1B7,0x81B6,0x4076,
0x0072,0xC1B2,0x81B3,0x4073,0x01B1,0xC071,0x8070,0x41B0,
0x0050,0xC190,0x8191,0x4051,0x0193,0xC053,0x8052,0x4192,
0x0196,0xC056,0x8057,0x4197,0x0055,0xC195,0x8194,0x4054,
0x019C,0xC05C,0x805D,0x419D,0x005F,0xC19F,0x819E,0x405E,
0x005A,0xC19A,0x819B,0x405B,0x0199,0xC059,0x8058,0x4198,
0x0188,0xC048,0x8049,0x4189,0x004B,0xC18B,0x818A,0x404A,
0x004E,0xC18E,0x818F,0x404F,0x018D,0xC04D,0x804C,0x418C,
0x0044,0xC184,0x8185,0x4045,0x0187,0xC047,0x8046,0x4186,
0x0182,0xC042,0x8043,0x4183,0x0041,0xC181,0x8180,0x4040,
};

/*function returns CRC as an unsigned short*/
/*parameter puchMsg: the message used to calculate CRC*/
/*parameter usDataLen: the number of bytes in the message*/
unsigned int CRC16(unsigned int * puchMsg,unsigned int usDataLen)
{
    unsigned int uchCRCHi = 0xFF ; /*high byte initialization of CRC*/
    unsigned int uchCRCLo = 0xFF ; /*low byte initialization of CRC*/
    unsigned int uIndex ; /*CRC lookup table index*/
    unsigned int hi,low;
    while (usDataLen--) /*complete the entire message buffer*/
    {
        uIndex = uchCRCLo ^ *puchMsg++ ; /*CalcCRC*/
        hi = tblCRC[uIndex] >> 8;
        low = tblCRC[uIndex] & 0xff;
        uchCRCLo = uchCRCHi ^ hi;
        uchCRCHi = low;
    }
    return (uchCRCHi << 8 | uchCRCLo) ;
}

```

## 4. Unit and Dimension Description

Physical Quantity	Unit	Magnification	Description
Voltage (including AC and DC)	V	10	16-bit unsigned integer ranging from 0 to 65,535, corresponding to 0 V to 6,553.5 V
Current (including AC and DC)	A	10	16-bit unsigned integer ranging from 0 to 65,535, corresponding to 0 A to 6,553.5 A 16-bit signed integer ranging from -32,767 to 32,767, corresponding to -3,276.7 A to 3,276.7 A

Frequency	Hz	100	16-bit unsigned integer ranging from 0 to 65,535, corresponding to 0 Hz to 655.35 Hz
Power (including AC and DC)	W	1	16-bit unsigned integer ranging from 0 to 65,535, corresponding to 0 W to 65,535 W
Power factor	/	1000	16-bit signed integer ranging from -32,767 to 32,767 (e.g., 998 indicates a power factor of 0.998; and -900 (0×FC7C) indicates a power factor of -0.900.)
AC side capacity	kWh	10	16-bit unsigned integer ranging from 0 to 65,535, corresponding to 0 kWh to 6,553.5 kWh; 32-bit unsigned integer ranging from 0 to 4,294,967,295, corresponding to 0 kWh to 429,496,729.5 kWh; (e.g., 1 indicates 0.1 kWh and 10 indicates 1 KWH)
Battery side capacity	AH	1	16-bit unsigned integer ranging from 0 to 65,535, corresponding to 0 AH to 65,535 AH; 32-bit unsigned integer ranging from 0 to 4,294,967,295, corresponding to 0 AH to 4 294 967 295 AH
Temperature	°C	10	16-bit signed integer ranging from -32,767 to 32,767, corresponding to -3,276.7°C to 3,276.7°C
Battery set voltage	V	10	All battery set voltages in this protocol are in the unified dimension of 12 V batteries, that is, all battery set voltages are converted to the corresponding voltage of 12 V. If the rated voltage of the battery is 48 V and the actual set voltage is 57.6 V, the set value is 57.6 V/4=14.4 V, and the value converted for the register is 14.4*10=144.

Note: When 32-bit data occupies two registers, the data is stored in the register in small-endian mode, that is, the low bytes of data are in the low address of the register, and the high bytes are in the high address of the register. If the 32-bit data 0×12345678 is stored at 0×0001 and 0×0002, the order in the register table is 0×0001=0×5678 and 0×0002=0×1234.

## MODBUS Protocol for Energy Storage Inverter - Register Address Table

## Note:

1. The register displayed in gray font is invalid for the energy storage inverter.
2. Magnification refers to the multiple of the actual value than the register value. If the magnification is 0.1, the actual value is the register value multiplied by 0.1.

Address	Length	Name	English Name	R/W	Magnification	Unit	Display Format	Signed/Unsigned	Minimum	Maximum	Default	Remark
<b>P00 Product Information Area</b>												
A	1	小版本号	MinorVersion	R	1	-	%d	Unsigned				Reserved
B	1	产品类型	MachType	R	1	-	%d	Unsigned				Product type 00 (domestic controller) 01 (controller for street light) 03 (grid-connected inverter) 04 (all-in-one solar charger inverter) 05 (power frequency off-grid)
C	8	保留	ProductInfoReversed01	R	1	-	%s	Unsigned				Reserved
14	2	软件版本	SoftWareVersion	R	1	-	%d	Unsigned				0x0014: APP version (e.g., 100 for V1.00) 0x0015: BOOTLOADER version (e.g., 100 for V1.00), reserved
16	2	硬件版本	HardWareVersion	R	1	-	%d	Unsigned				0x0016: control panel version (e.g., 100 for V1.00) 0x0017: power amplifier board version (e.g., 100 for V1.00), reserved
18	2	保留	ProductInfoReversed02	R	1	-	%x	Unsigned				Reserved
1A	1	控制器、设备地址	Rs485Addr	R	1	-	%d	Unsigned				Rs485 address, which is read-only
1B	1	机型编码	MachModelNum2	R	1	-	%d	Unsigned				
1C	2	RS485协议版本	RS485Version	R	1	-	%x	Unsigned				0x001C: protocol version (e.g., 100 for V1.00) 0x001D: reserved
1E	2	生产日期	ManufactureDate	R	1	-	%x	Unsigned				0x001E: high byte: year, low byte: month 0x001F: high byte: day, low byte: hour
20	1	产地编码	ProductAreaCode	R	1	-	%x	Unsigned				0: Shenzhen 1: Dongguan
21	20	软件编译时间	CpuBuildTime	R	1	-	%s	Unsigned				String format, with the low bytes of each register valid and the high bytes invalid
35	20	产品序列号字符串	ProductSNStr	R	1	-	%s	Unsigned				String format, with the low bytes of each register valid and the high bytes invalid
49	1	保留	ProductInfoReversed03	R	1	-	%x	Unsigned				
<b>P01 DC Data Area</b>												
100	1	蓄电池电量SOC	BatSoc	R	1	-	%d	Unsigned				Percentage of remaining battery power
101	1	蓄电池电压	BatVolt	R	0.1	V	%.1fV	Unsigned				Battery voltage (e.g., 485 for 48.5 V)
102	1	电池电流	ChargeCurr	R	0.1	A	%.1fA	Signed				Battery current (e.g., 500 for 50.0A)
103	1	电池温度	DeviceBatTemper	R	0.1	°C	%.1f°C	Signed				Battery temperature
104	1	保留	DcDataReserved00	R	0.1	V	%.1fV	Unsigned				Reserved
105	1	保留	DcDataReserved01	R	0.01	A	%.2fA	Unsigned				Reserved
106	1	保留	DcDataReserved02	R	1	W	%d	Unsigned				Reserved
107	1	太阳能板1电压	Pv1Volt	R	0.1	V	%.1fV	Unsigned				Voltage of PV panel 1
108	1	太阳能板1电流	Pv1Curr	R	0.1	A	%.1fA	Unsigned				Current of PV panel 1
109	1	太阳能板1功率	Pv1ChargePower	R	1	W	%d	Unsigned				Power of PV panel 1
10A	1	太阳能板总功率	PvTotalPower	R	1	-	%d	Unsigned				Total PV power
10B	1	电池充电状态	ChargeState	R	1	-	%d	Unsigned				0x0000: Charge off 0x0001: Quick charge 0x0002: Const voltage charge 0x0004: Float charge 0x0005: Reserved 0x0006: Li battery activate 0x0008: Full
10C	2	保留	DcDataReserved04	R	1	-	%d	Unsigned				Reserved
10E	1	充电总功率	ChargePower	R	1	W	%.6dW	Unsigned				PV charging power + AC charging power
10F	1	太阳能板2电压	Pv2Volt	R	0.1	V	%.1fV	Unsigned				Voltage of PV panel 2
110	1	太阳能板2电流	Pv2Curr	R	0.1	A	%.1fA	Unsigned				Current of PV panel 2
111	1	太阳能板2功率	Pv2ChargePower	R	1	W	%d	Unsigned				Power of PV panel 2
<b>P02 Inverter Data Area</b>												
200	4	当前故障位	CurrErrReg	R	1	-	%x	Unsigned				Each fault bit represents a fault, with a total of 64 bits. This register is used by the internal debugging.
204	4	当前故障码	CurrFcode	R	1	-	%d	Unsigned				There are four addresses. Each address stores a fault code corresponding to the current fault. Four fault codes can be displayed at the same time. 0 indicates no fault. If there are two faults, battery under-voltage and inverter overload, the following information is displayed: 0x204: 01 0x205: 14 0x206: 00 0x207: 00
208	4	保留	ReservedInvData0	R	2	-	%x	Unsigned				Reserved
20C	3	当前时间	SysDateTime	RW	1	-	%zdt	Unsigned				0x020C: high byte: year, low byte: month 0x020D: high byte: day, low byte: hour 0x020E: high byte: minute, low byte: second The register can be set to adjust the RTC clock.
20F	1	并网倒计时	GridOnRemainTime	R	1	s	%d	Unsigned				









Address	Length	Name	English Name	R/W	Magnification	Unit	Display Format	Signed/Unsigned	Minimum	Maximum	Default	Remark
F031	1	总运行天数	WorkDaysTotal	R	1	d	%d	Unsigned				Cumulative value of power generated to the grid
F032	2	累计并网电量	GridEnergyTotal	R	0.1	kWh	%.1fkWh	Unsigned				
F034	2	蓄电池累计充电安时数	BatChgAHTotal	R	1	AH	%d	Unsigned				
F036	2	蓄电池累计放电安时数	BatDischgAHTotal	R	1	AH	%d	Unsigned				
F038	2	PV累计发电量	GeneratEnergyTotal	R	0.1	kWh	%.1fkWh	Unsigned				
F03A	2	负载累计用电量	UsedEnergyTotal	R	0.1	kWh	%.1fkWh	Unsigned				
F03C	1	市电当天充电电量	LineChgEnergy1Day	R	1	AH	%d	Unsigned				AC charging power (AH) for the day
F03D	1	负载当天从市电消耗电量	LoadConsumLine1day	R	0.1	kWh	%.1fkWh	Unsigned				
F03E	1	逆变当天工作时间	InvWorkTimeToday	R	1	min	%dmin	Unsigned				
F03F	1	旁路当天工作时间	LineWorkTimeToda	R	1	min	%dmin	Unsigned				
F040	3	开机时间	PowerOnTime	R	1		%d	Unsigned				Refer to the time register for the current time format.
F043	3	上次均衡充电完成时间	LastEquaChgTime	R	1		%d	Unsigned				Refer to the time register for the current time format.
F046	2	市电累计用电量	LineChgEnergyTotal	R	1	AH	%d	Unsigned				
F048	2	负载累计从市电消耗电量	LoadConsumLineTotal	R	0.1	kWh	%.1fkWh	Unsigned				Cumulative load power consumed from the battery side
F04A	1	逆变累计工作时间	InvWorkTimeTotal	R	1	h	%dh	Unsigned				
F04B	1	旁路累计工作时间	LineWorkTimeTotal	R	1	h	%dh	Unsigned				
F04C	1	市电充电电量kwh	LineChgKwHTday	R	1		%d	Unsigned				
F04D	1	保留	EnergyReserved3	R	1		%d	Unsigned				
<b>P10 Fault Record</b>												
F800	16	故障记录0	FaultHistoryRecord00	RW	1		%d	Unsigned				
F810	16	故障记录1	FaultHistoryRecord01	RW	1		%d	Unsigned				
F820	16	故障记录2	FaultHistoryRecord02	RW	1		%d	Unsigned				
F830	16	故障记录3	FaultHistoryRecord03	RW	1		%d	Unsigned				
F840	16	故障记录4	FaultHistoryRecord04	RW	1		%d	Unsigned				
F850	16	故障记录5	FaultHistoryRecord05	RW	1		%d	Unsigned				
F860	16	故障记录6	FaultHistoryRecord06	RW	1		%d	Unsigned				
F870	16	故障记录7	FaultHistoryRecord07	RW	1		%d	Unsigned				
F880	16	故障记录8	FaultHistoryRecord08	RW	1		%d	Unsigned				
F890	16	故障记录9	FaultHistoryRecord09	RW	1		%d	Unsigned				
F8A0	16	故障记录10	FaultHistoryRecord10	RW	1		%d	Unsigned				
F8B0	16	故障记录11	FaultHistoryRecord11	RW	1		%d	Unsigned				
F8C0	16	故障记录12	FaultHistoryRecord12	RW	1		%d	Unsigned				
F8D0	16	故障记录13	FaultHistoryRecord13	RW	1		%d	Unsigned				
F8E0	16	故障记录14	FaultHistoryRecord14	RW	1		%d	Unsigned				
F8F0	16	故障记录15	FaultHistoryRecord15	RW	1		%d	Unsigned				
F900	16	故障记录16	FaultHistoryRecord16	RW	1		%d	Unsigned				
F910	16	故障记录17	FaultHistoryRecord17	RW	1		%d	Unsigned				
F920	16	故障记录18	FaultHistoryRecord18	RW	1		%d	Unsigned				
F930	16	故障记录19	FaultHistoryRecord19	RW	1		%d	Unsigned				
F940	16	故障记录20	FaultHistoryRecord20	RW	1		%d	Unsigned				
F950	16	故障记录21	FaultHistoryRecord21	RW	1		%d	Unsigned				
F960	16	故障记录22	FaultHistoryRecord22	RW	1		%d	Unsigned				
F970	16	故障记录23	FaultHistoryRecord23	RW	1		%d	Unsigned				
F980	16	故障记录24	FaultHistoryRecord24	RW	1		%d	Unsigned				
F990	16	故障记录25	FaultHistoryRecord25	RW	1		%d	Unsigned				
F9A0	16	故障记录26	FaultHistoryRecord26	RW	1		%d	Unsigned				
F9B0	16	故障记录27	FaultHistoryRecord27	RW	1		%d	Unsigned				
F9C0	16	故障记录28	FaultHistoryRecord28	RW	1		%d	Unsigned				
F9D0	16	故障记录29	FaultHistoryRecord29	RW	1		%d	Unsigned				
F9E0	16	故障记录30	FaultHistoryRecord30	RW	1		%d	Unsigned				
F9F0	16	故障记录31	FaultHistoryRecord31	RW	1		%d	Unsigned				
FA00	16	意大利参数测试记录	AutoTestRecord	RW	1		%d	Unsigned				
FA10	1	保留	RecordReserved0	R	1		%d	Unsigned				
FA11	1	保留	RecordReserved1	R	1		%d	Unsigned				

END

Note: The 0x0438–0x439 is the online upgrade command entry address.

## MODBUS Register Map

Start Address	End Address	Length	Area
000AH	00FFH	00F6H	Product parameter information area
0100H	01FFH	0100H	Device live message data area
0200H	02FFH	0100H	Device live message data area
0300H	6FFFH	6D00H	Reserve area
7000H	7FFFH	1000H	Device live message data area
8000H	DFFFH	6000H	Reserve area
DF00H	DF1FH	0020H	Device control area
DF20H	DFFFH	00E0H	Debug data area
E000H	E0FFH	0100H	User setting area for controller parameters
E100H	E1FFH	0100H	Factory setting area for inverter parameters
E200H	E2FFH	0100H	User setting area for inverter parameters
E300H	E3FFH	0100H	Factory setting area for controller parameters
E800H	E8FFH	0100H	Grid-connected product parameter information area
E900H	E97FH	0080H	User setting area for grid-connected inverter parameters
E980H	EA7FH	00FFH	Factory setting area for grid-connected inverter parameters
EA80H	EAFFH	0080H	Factory setting area for energy storage inverter parameters
F000H	F7FFH	0800H	Historical data
F800H	FFFFH	0800H	Historical data

### Data Area of Grid-connected Inverter

Grid-connected inverter data area: 0x7000–0x70FF (16 bytes)

Energy storage inverter data area: 0x7100–0x717F (16 bytes)

Reserve area: 0x7180–0xDEFF (28,032 W)

Device control area: 0xDF00–0xDF1F (32 W)

<b>Debug data area: 0xDF20–0xDFFF (224 W)</b>
<b>Area occupied by other devices: 0xE000–0xE7FF (2,048 W)</b>
<b>Product parameter information area: 0xE800–0xE8F</b>
<b>User parameter setting area: 0xE900–0xE97F (128 W)</b>
<b>Grid-connected inverter parameter area: 0xE980–0x</b>
<b>Energy storage inverter parameter area: 0xEA80–0x</b>
<b>Reserve area: 0xEB00–0xEFFF (1,280 W)</b>
<b>Area occupied by other devices: 0xF000–0xE7FF (2,048 W)</b>
<b>Historical record of grid-connected/energy storage in</b>

## US Register Area

Device Type
Domestic controller, all-in-one solar charger inverter, off-grid inverter, street light controller
Domestic controller, all-in-one solar charger inverter, street light controller
All-in-one solar charger inverter, off-grid inverter
Reserved (lithium battery&BMS)
Grid-connected/Energy storage inverter
Grid-connected/Energy storage inverter
General
General
Domestic controller, all-in-one solar charger inverter, off-grid inverter, street light controller
All-in-one solar charger inverter, off-grid inverter
All-in-one solar charger inverter, off-grid inverter
Domestic controller, street light controller
Grid-connected/Energy storage inverter
Grid-connected/Energy storage inverter
Grid-connected inverter
Energy storage inverter
Domestic controller
Grid-connected/Off-grid/Energy storage inverter

## d/Off-grid/Energy Storage Inverter

256 W)

128 W)

048 W)
7F (256 W)
)
EA7F (256 W)
EAFF (128 W)
048 W)
verter: 0xF800–0xFFFF